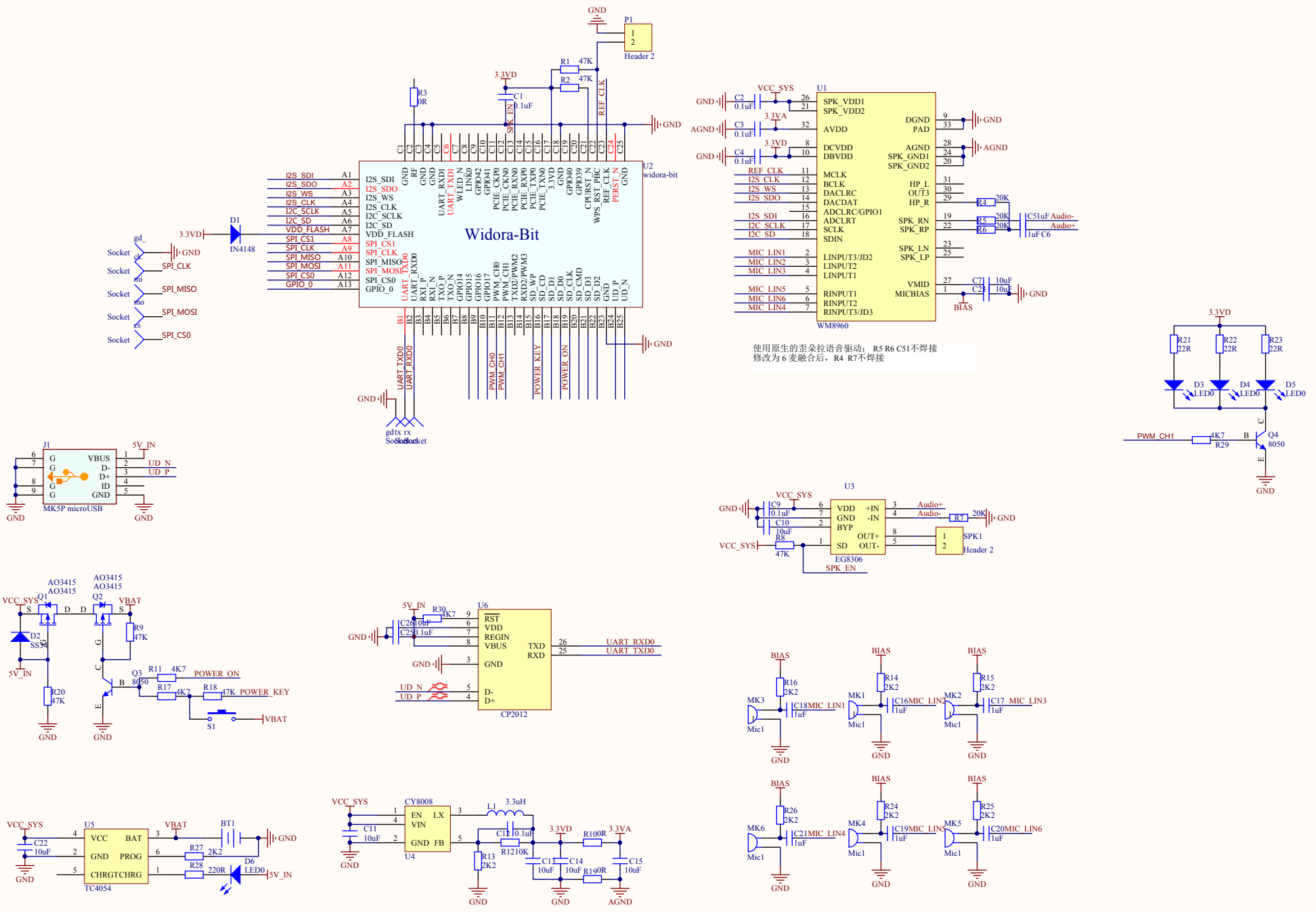
Widora-Bit
U2
widora-bit
P1
Header 2
D1
IN4148
3.3VD
GND
SPI_CLK
SPI_MISO
SPI_MOSI
SPI_CS0
U1
WM8960
VCC_SYS
3.3VA
AGND
使用原生的歪朵拉语音驱动： R5 R6 C51不焊接
修改为 6 麦融合后， R4 R7不焊接
J1
MK5P microUSB
5V_IN
UD_N
UD_P
U3
EG8306
SPK_EN
SPK1
Header 2
Q1
Q2
AO3415
VBAT
Q3
8050
POWER_ON
POWER_KEY
S1
U6
CP2012
UART_RXD0
UART_TXD0
U5
TC4054
BT1
D6
LED0
U4
CY8008
L1
3.3uH
MK1
MK2
MK3
MK4
MK5
MK6
Mic1
BIAS
Q4
8050
PWM_CH1